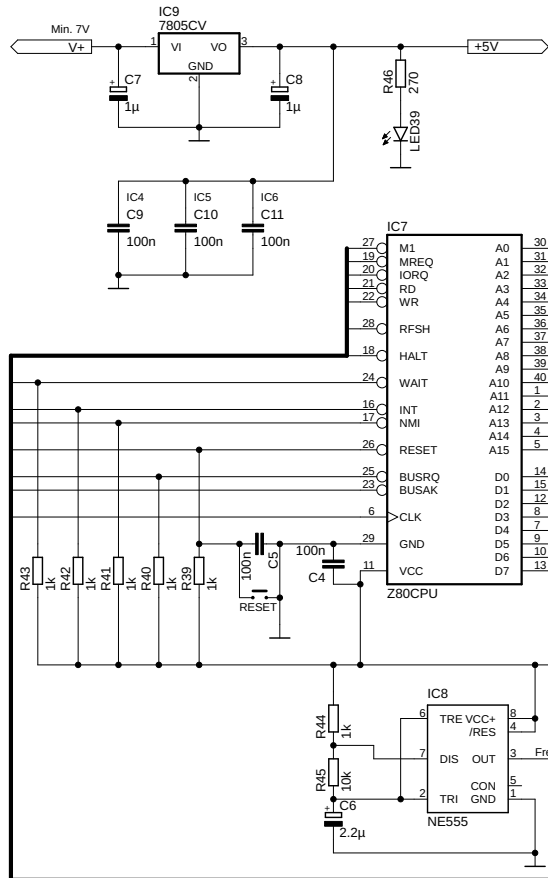
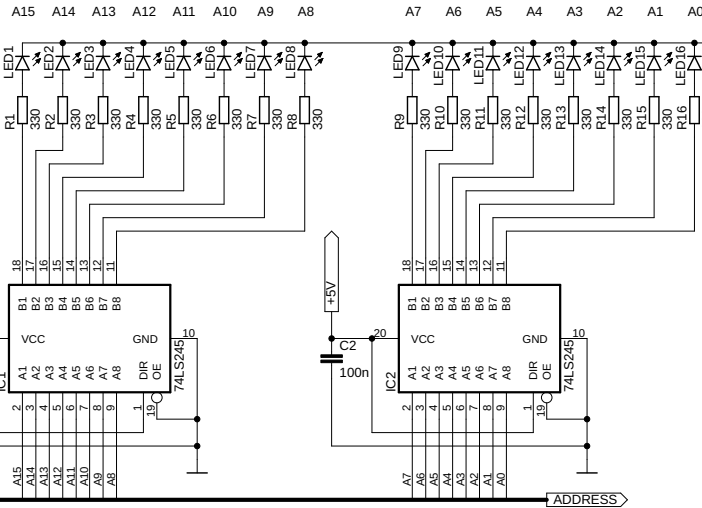


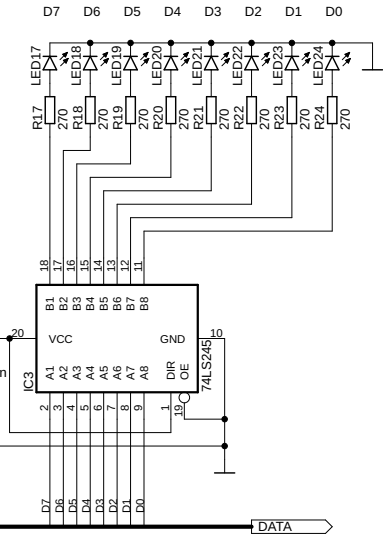
Prototype ordinateur 8bits Z80  
 Circuit CPU + Horloge système + Affichage bus  
 Auteur: Jelora  
 Date: 31/03/2019



ADRESS BUS



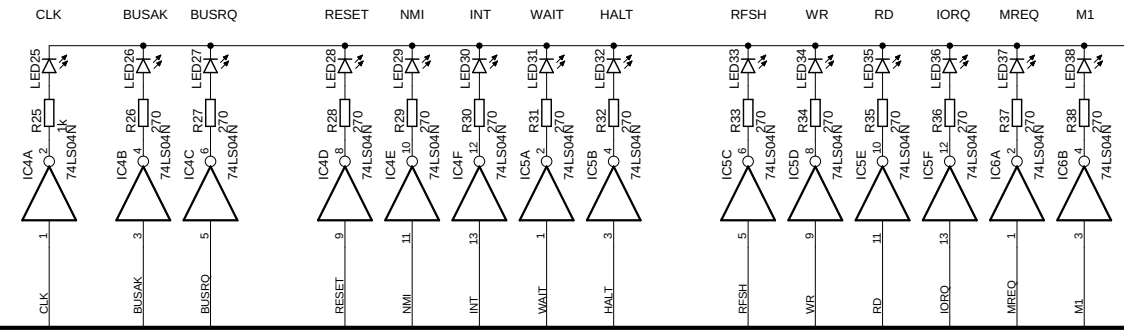
DATA BUS



CPU BUS CONTROL

CPU CONTROL

SYSTEM CONTROL



CONTROL